

FIG. 1

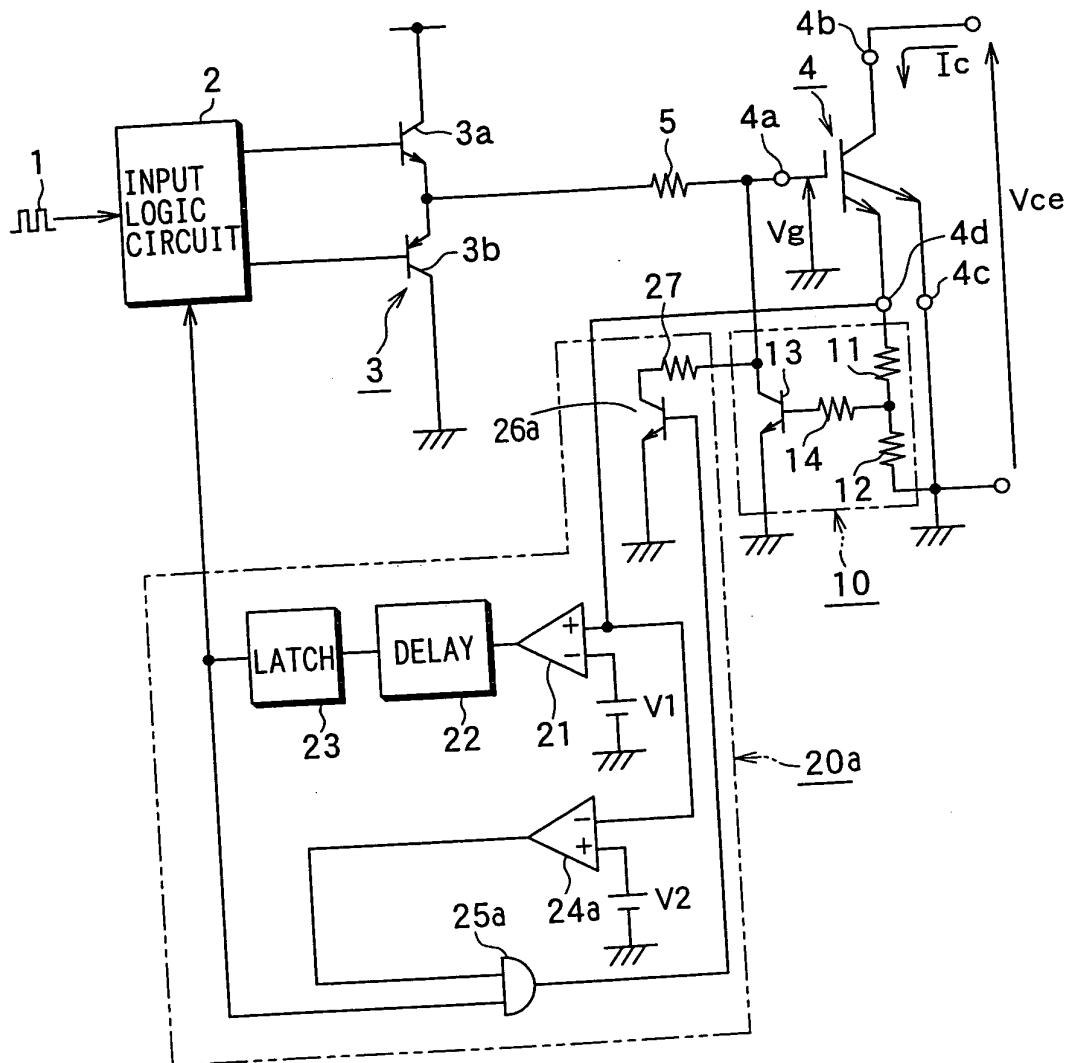


FIG. 2A

I_c

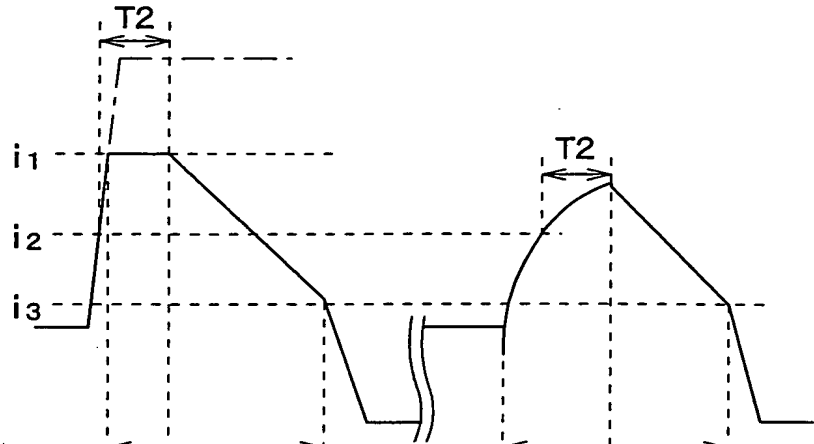


FIG. 2B

OUTPUT OF
LATCH CIRCUIT

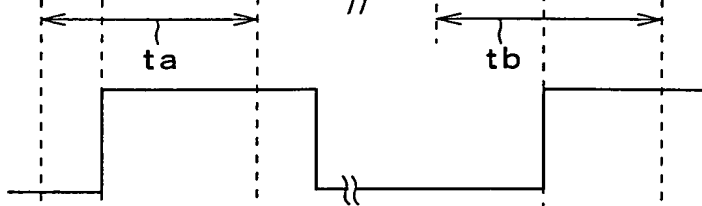


FIG. 2C

V_g

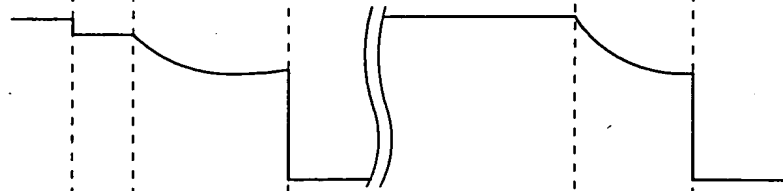
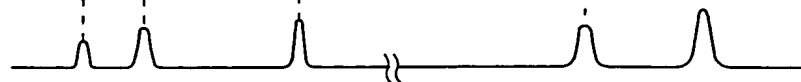


FIG. 2D

V_{ce}



03649746 "036904
TOP SECRET

FIG. 3

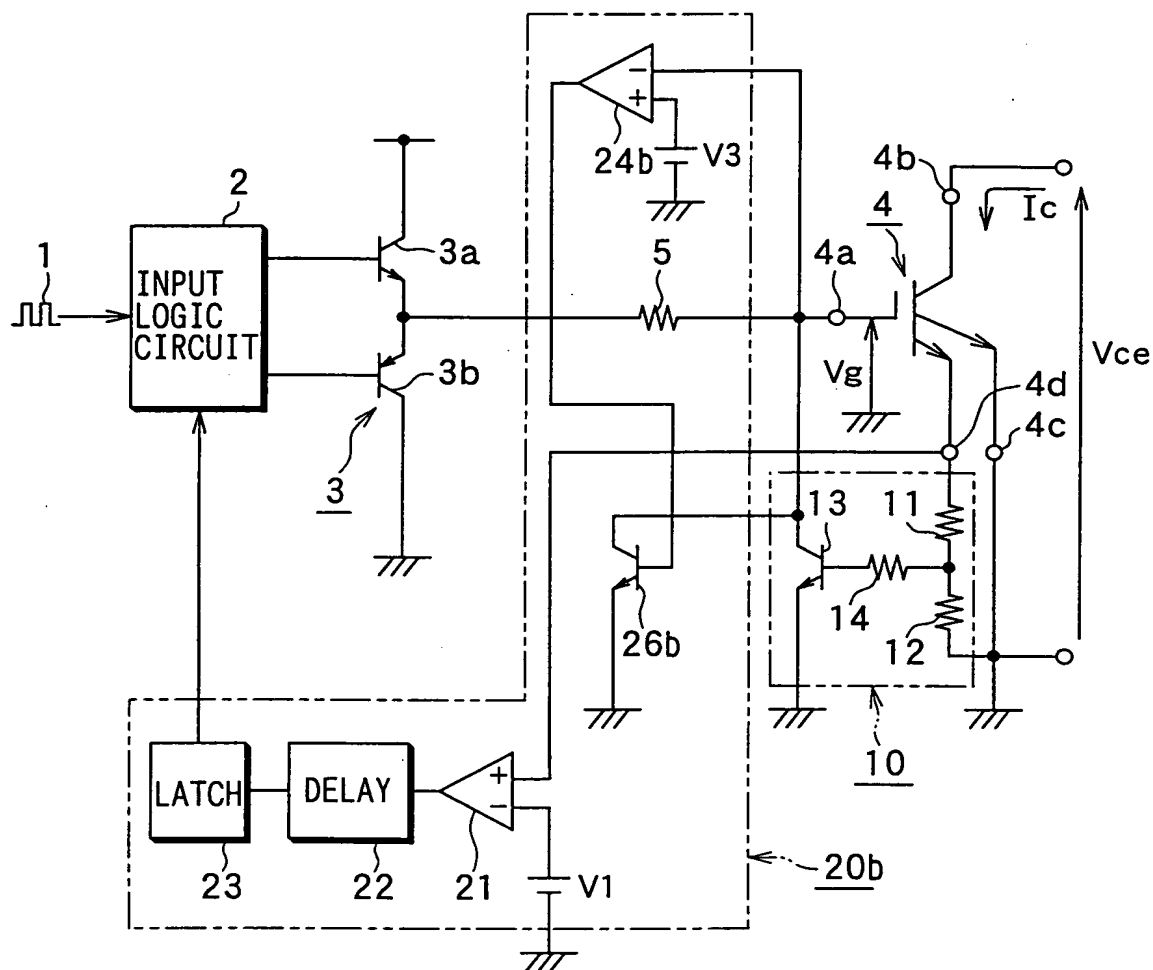


FIG. 4

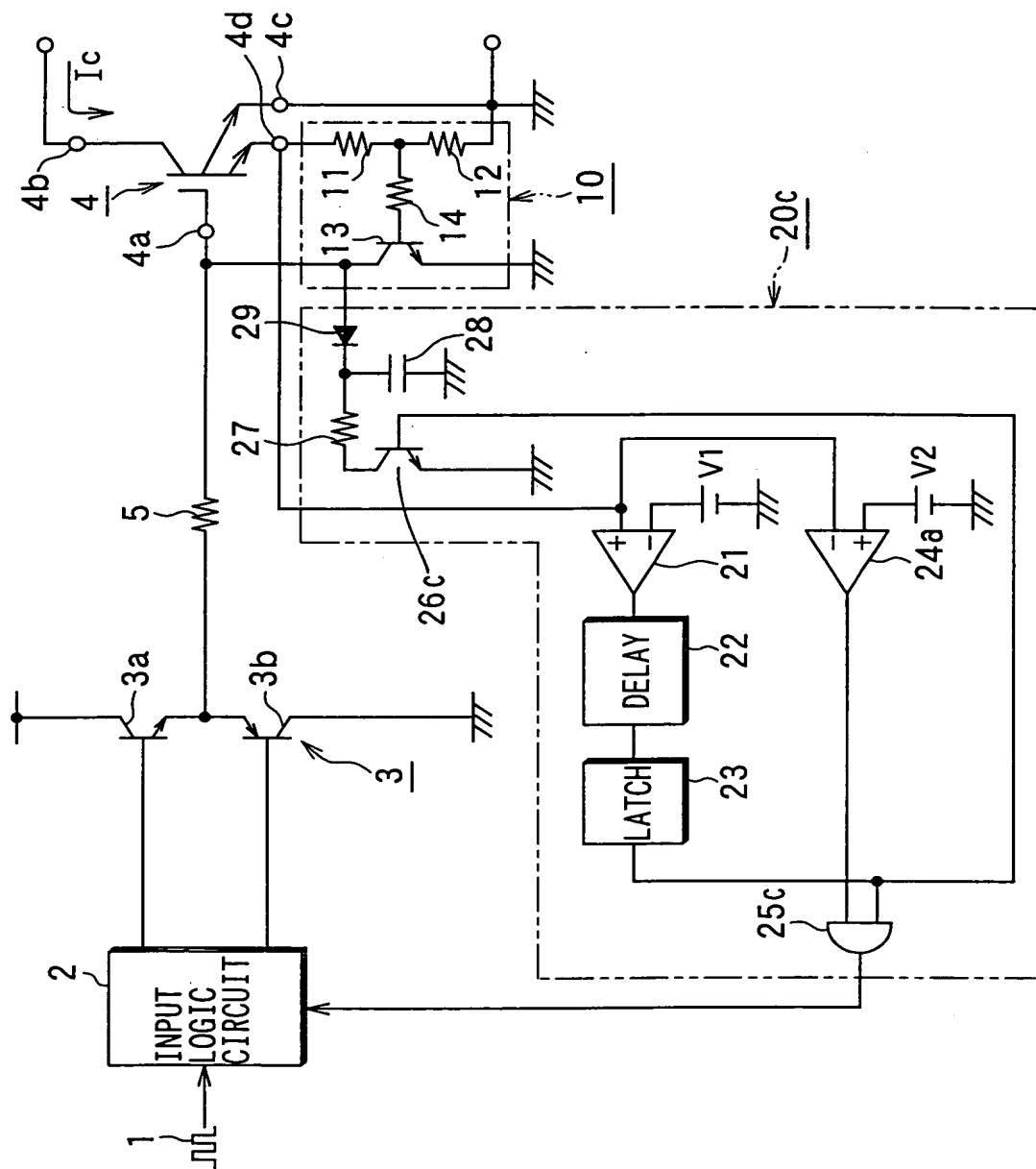


FIG. 5A

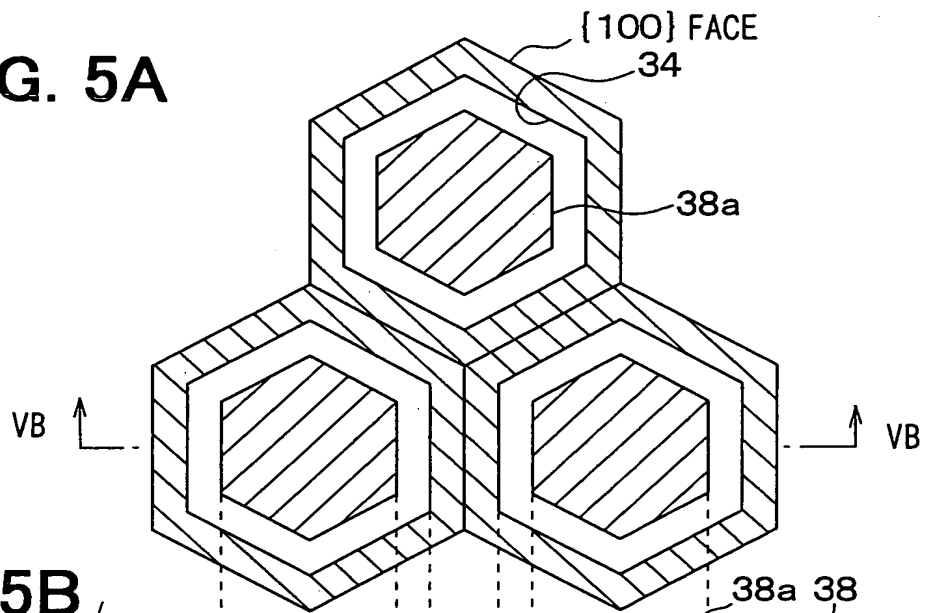


FIG. 5B

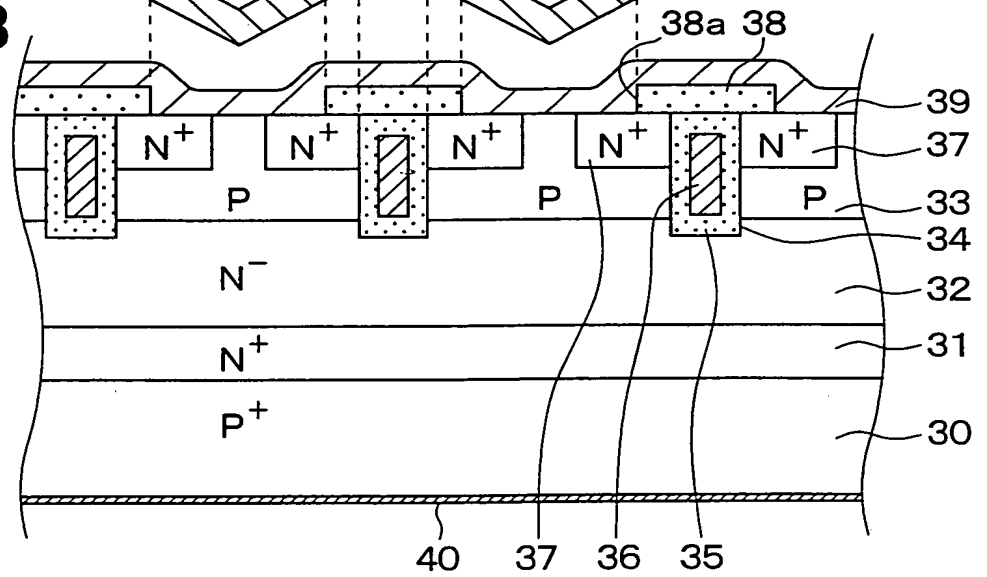


FIG. 6

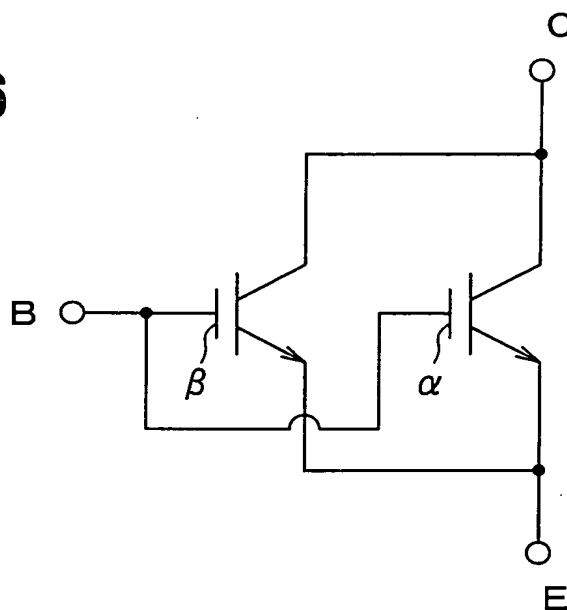


FIG. 7

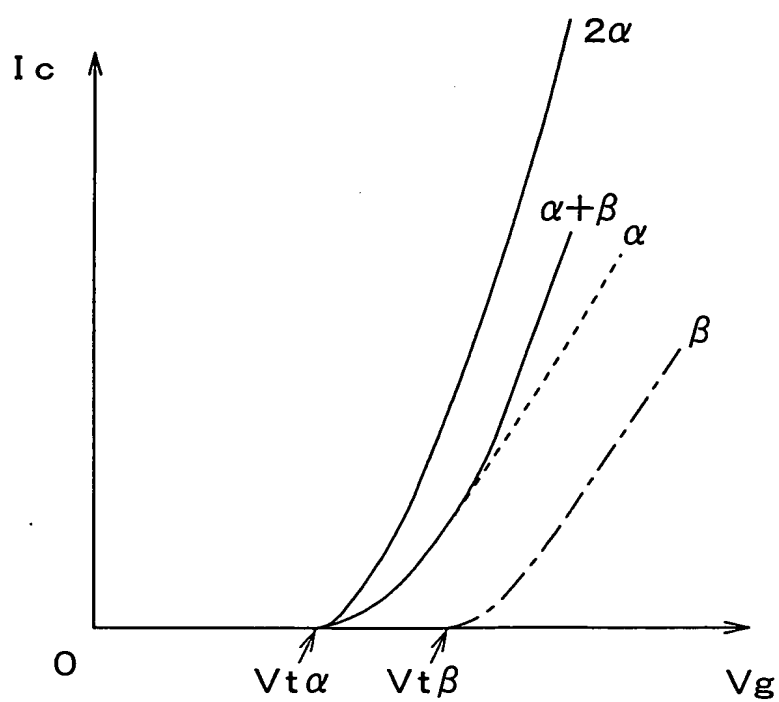


FIG. 8

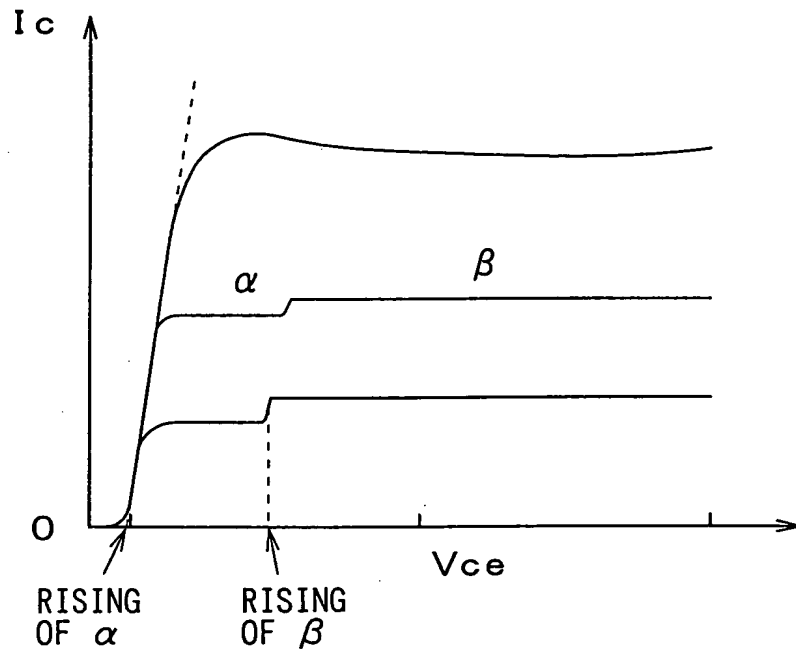


FIG. 9A

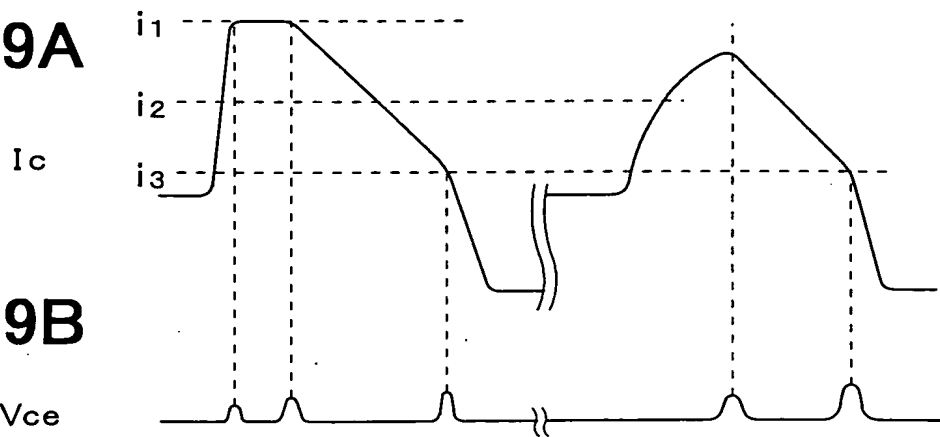


FIG. 9B

FIG. 10A

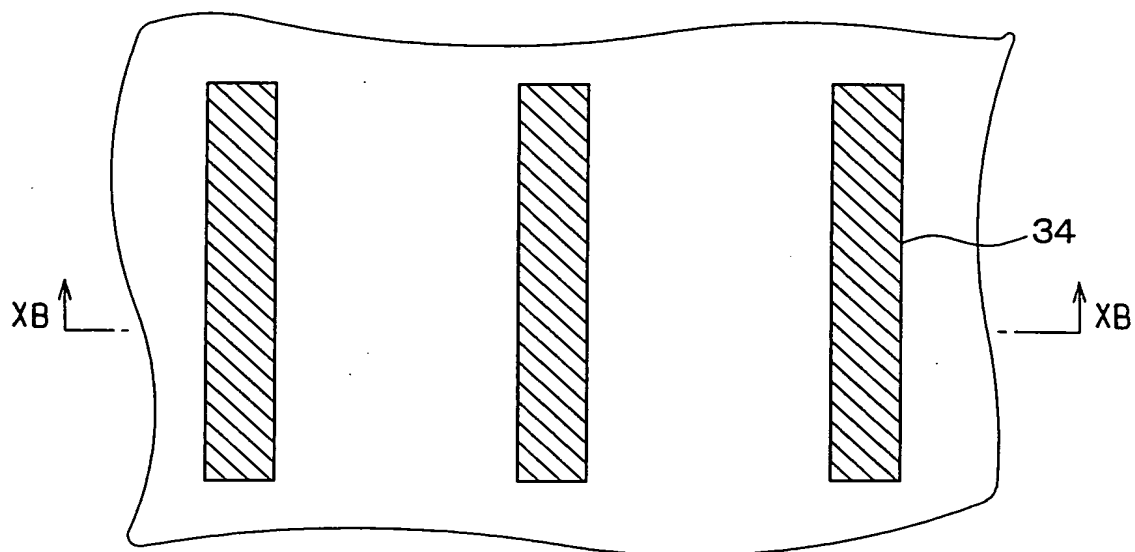


FIG. 10B

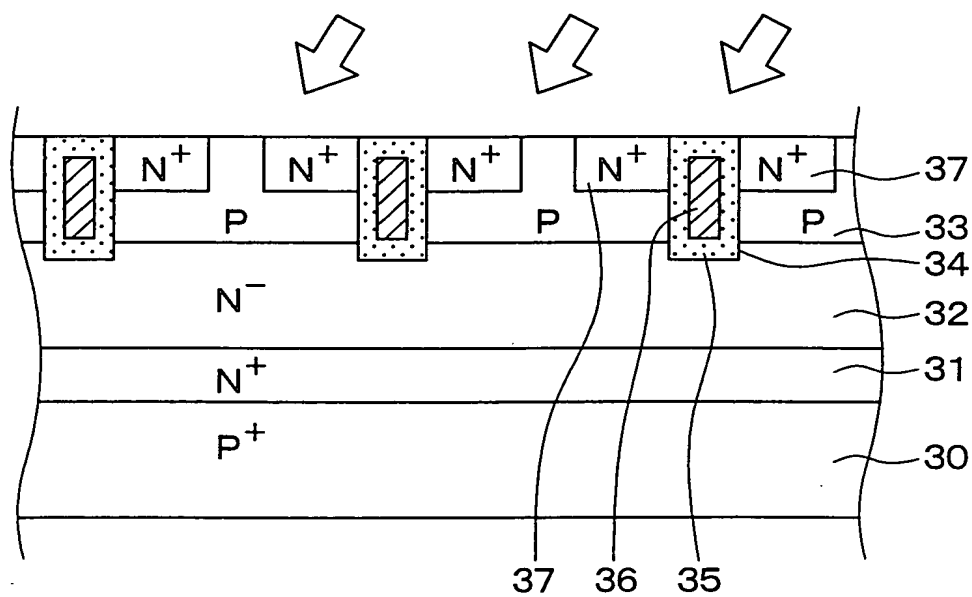


FIG. 11
PRIOR ART

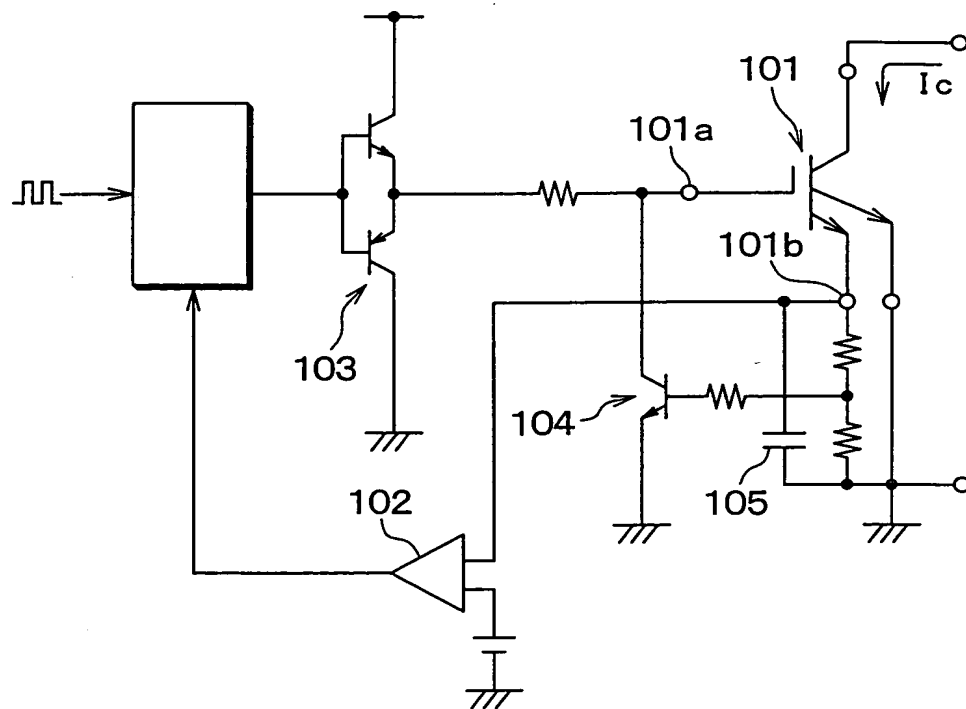


FIG. 12
PRIOR ART

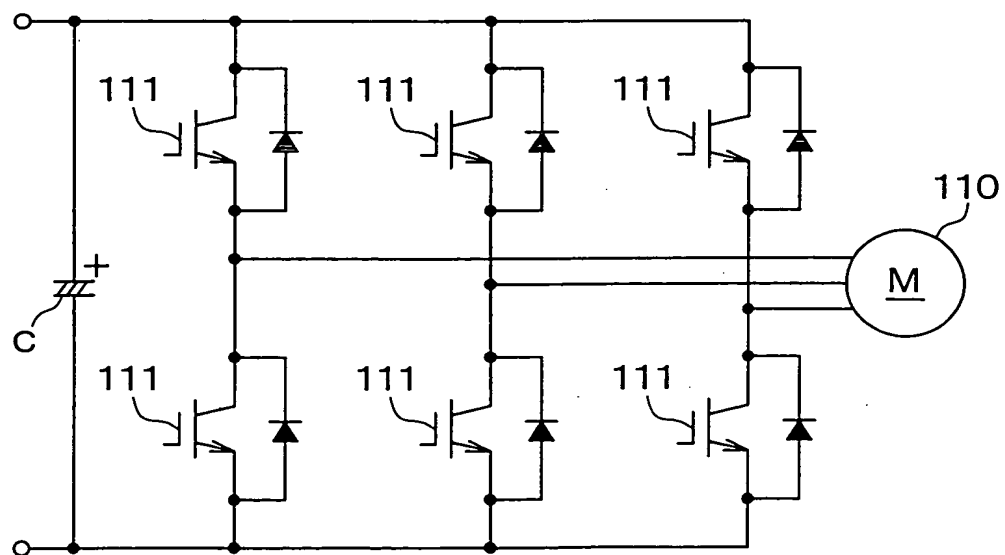


FIG. 13

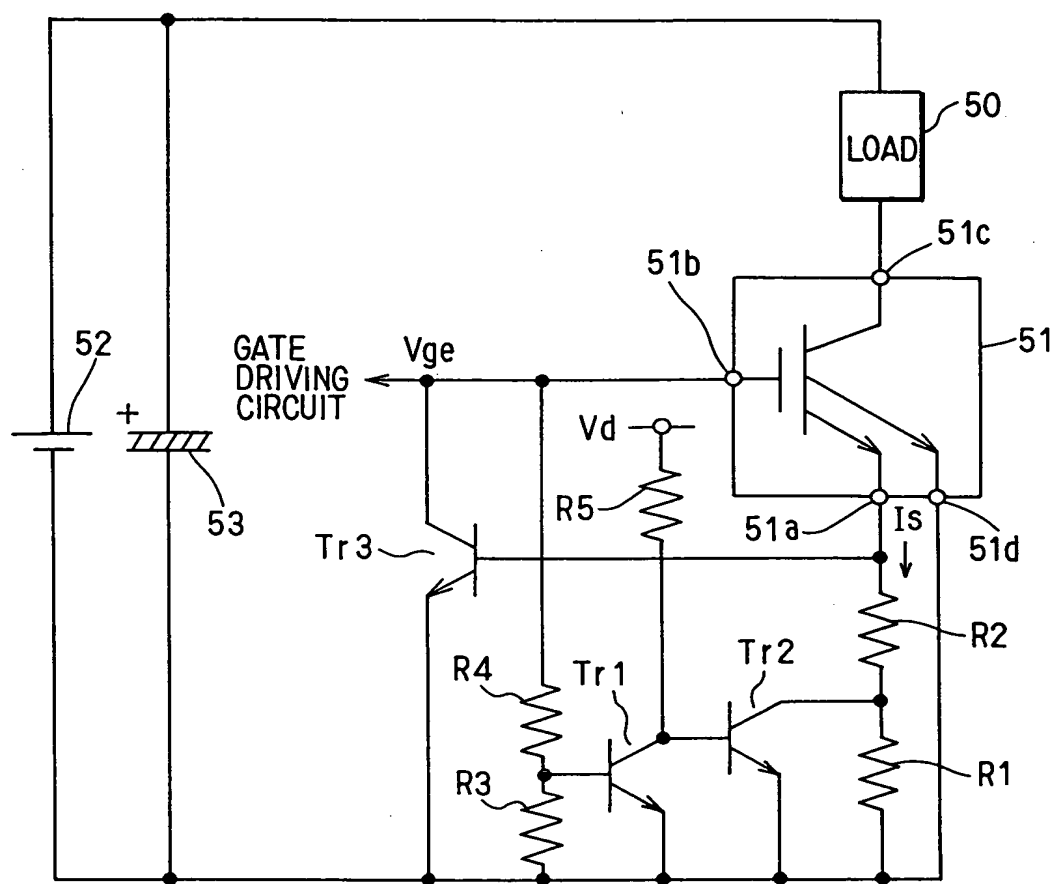


FIG. 14

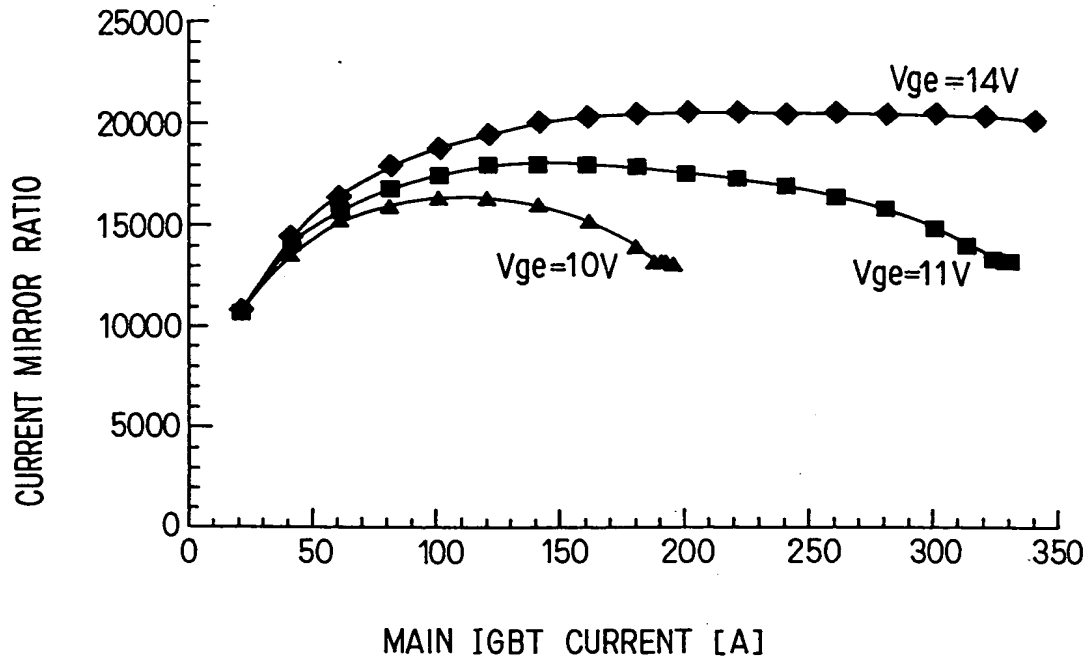


FIG. 15

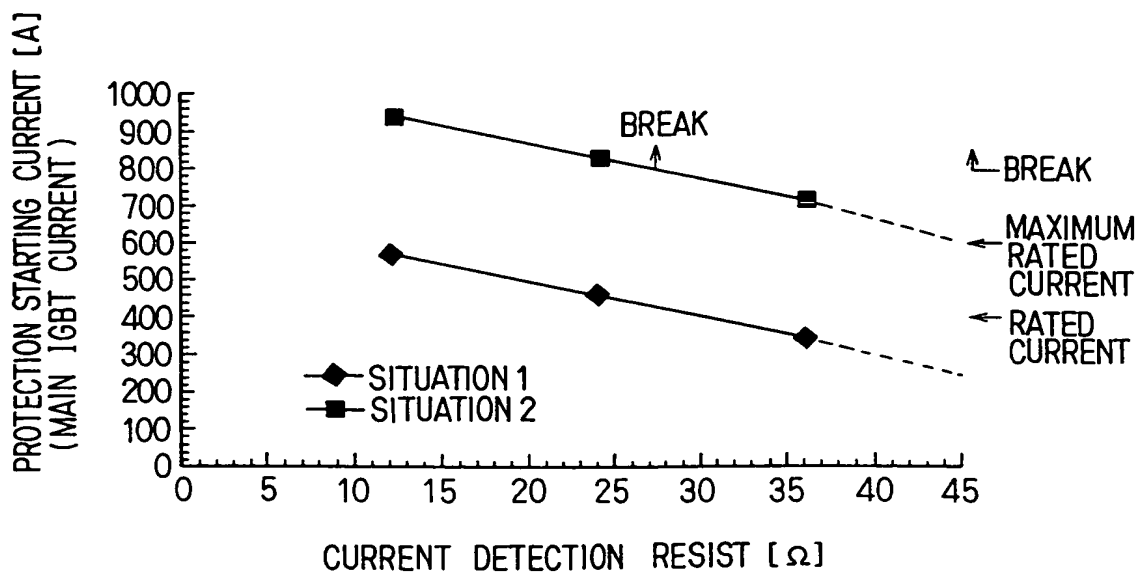


FIG. 16

